

PTO-1449 REPRODUCED  <b>INFORMATION DISCLOSURE CITATION IN AN APPLICATION</b>  <b>October 22, 2003</b>  (Use several sheets if necessary)	ATTORNEY DOCKET NO. 2037.1003-013		CONT. OF APPLICATION NO. 10/278,195	
	APPLICANT Richard C. Foss			
	FILING DATE		CONFIRMATION NO.	GROUP <b>2824</b>

U.S. PATENT DOCUMENTS				
EXAM- INER INI- TIAL	REF. NO.	DOCUMENT NUMBER	ISSUE DATE / PUBLICATION DATE	NAME
H2	AA	4,208,730	06-17-80	Dingwall et al.
H2	AB	4,558,435	12-10-85	Hsieh
H2	AC	4,920,517	4-24-90	Yamauchi et al.
H2	AD	4,926,382	05-15-90	Sakui et al.
H2	AE	5,258,957	11-02-93	Seta et al.
H2	AF	5,280,450	01-18-94	Nakagome et al.
H2	AG	5,353,255	10-04-94	Komuro
H2	AH	5,361,233	11-01-94	Kotani
H2	AI	5,742,544	04-21-98	Foss
H2	AJ	5,226,139	07-1993	Fujishima
	AK			
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EXAMINER	<b>H'EN NGUYEN</b>	DATE CONSIDERED	<b>5/15/04</b>
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	APPLICANT <b>Richard C. Foss</b>		
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FOREIGN PATENT DOCUMENTS						
		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION YES NO	
<b>2824</b>	AL	0 503 504 A2	09-16-92	Europe		
	AM	0 359 203 A2	03-21-90	Europe		
	AN	61-48194	03-08-86	Japan	X	
	AO	1-241093	09-26-89	Japan	X	
	AP	4-30385	1992	Japan		
	AQ	63-46696	1988	Japan		
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**OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)**

<b>HN</b>	AR	Inoue, Michihiro et al., "A 16-Mbit DRAM with a Relaxed Sense-Amplifier-Pitch Open-Bit-Line Architecture," IEEE Journal of Solid-State Circuits, Vol. 33, No. 5, October 1988, pp. 1104-1112.
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